

```
else
    clear(*port,pin);
bit_delay();
if bit_test(txchar,2)
    set(*port,pin);
else
    clear(*port,pin);
bit_delay();
if bit_test(txchar,3)
    set(*port,pin);
else
    clear(*port,pin);
bit_delay();
if bit_test(txchar,4)
    set(*port,pin);
else
    clear(*port,pin);
bit_delay();
if bit_test(txchar,5)
    set(*port,pin);
else
    clear(*port,pin);
bit_delay();
if bit_test(txchar,6)
    set(*port,pin);
else
    clear(*port,pin);
bit_delay();
if bit_test(txchar,7)
    set(*port,pin);
else
    clear(*port,pin);
bit_delay();
//
// stop bit
//
set(*port,pin);
bit_delay();
//
// char delay
//
bit_delay();
}

int main(void) {
//
// main
//
static unsigned char i,array_lo[NPTS],array_hi[NPTS];
//
// set clock divider to /1
//
CLKPR = (1 << CLKPCE);
CLKPR = (0 << CLKPS3) | (0 << CLKPS2) | (0 << CLKPS1) | (0 << CLKPS0);
//
// initialize output pins
// changed all "serial" to "pin"
```